

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

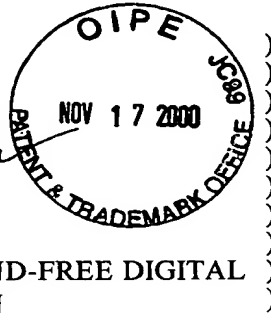
In re Patent Application of

Aven MATTISSON, et al.

Application No.: 09/580,632

Filed: May 30, 2000

For: LINEAR DEAD-BAND-FREE DIGITAL
PHASE DETECTION



Group Art Unit: 2858

Examiner: Unassigned

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**INFORMATION DISCLOSURE STATEMENT
TRANSMITTAL LETTER**

Assistant Commissioner for Patents
Washington, D.C. 20231

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Sir:


Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

- ☒ No additional fee for submission of an IDS is required.
- ☐ The fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- ☐ A certification under 37 C.F.R. § 1.97(e) is also enclosed.
- ☐ A certification under 37 C.F.R. § 1.97(e), a petition requesting consideration of the information disclosure statement, and the petition fee of \$130.00 (122) as set forth in 37 C.F.R. § 1.17(i) are also enclosed.
- ☐ Charge \$_____ to Deposit Account No. 02-4800 for the fee due.
- ☐ A check in the amount of \$_____ is enclosed for the fee due.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By: 
Kenneth B. Leffler
Registration No. 36,075

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Date: November 17, 2000

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Patent Application of

Sven MATTISSON, et al.

Application No.: 09/580,632

Filed: May 30, 2000

For: LINEAR DEAD-BAND-FREE DIGITAL
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Examiner: Unassigned

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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

1. U.S. Patent No. 6,049,233, issued to Shurboff on April 11, 2000.
2. U.S. Patent No. 5,631,582, issued to Fujikawa on May 20, 1997.
3. U.S. Patent No. 5,103,191, issued to Werker on April 7, 1992.
4. U.S. Patent No. 4,818,950, issued to Ranger on April 4, 1989.
5. D. P. Turner, "Phase-Locked Loop Phase Adjustment," IBM Technical Disclosure Bulletin, U.S., IBM Corp., New York, Vol. 15, No. 7, December 1972, pages 2080-2081.
6. European Search Report dated November 10, 2000 in connection with the above-identified application.


The documents are being submitted within 3 months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later, therefore no fee or certification is required under 37 C.F.R. § 1.97(b).

Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a Standard Search Report concerning this U.S. Application No. 09/580,632. This communication was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

To assist the Examiner, the published documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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